

United States Patent and Trademark Office

Page 1 of 2

SEP 2 2 2003

DIVIDUS STATES DEPARTMENT OF COMMERCE

United States Patent and Side on Policy
Address COMMISSIONER OF ATTEM STATEMARKS
Advanced States Page 22313-1450

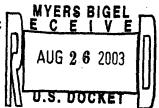
Advanced States Page 22313-1450

RET NO DRAWINGS TOT CLMS IND CLMS

APPL NO. | FILING OR 371 | GRP ART | FIL FEE REC'D | ATTY.DOCKET NO | DRAWINGS | TOT CLMS | IND CLMS | 10/601.937 | 06/24/2003 | 2811 | 750 | 5649-1137 | 12 | 20 | 3

CONFIRMATION NO. 7804

20792 MYERS BIGEL SIBLEY & SAJOVEC PO BOX 37428 RALEIGH, NC 27627



FILING RECEIPT
OC000000010722089

Date Mailed: 08/19/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Jae-Sun Yun, Gyeonggi-do, KOREA, REPUBLIC OF; Jin-Hyun Shin, Gyeonggi-do, KOREA, REPUBLIC OF;

Domestic Priority data as claimed by applicant

Foreign Applications

REPUBLIC OF KOREA 2002-0061720 10/10/2002

If Required, Foreign Filing License Granted: 08/19/2003

Projected Publication Date: 04/15/2004

Non-Publication Request: No

Early Publication Request: No

08-21-03 A09:15 IN

Title

Methods of forming trench isolated integrated circuit devices including grooves, and trench isolated integrated circuit devices so formed

Preliminary Class